

Figure 1

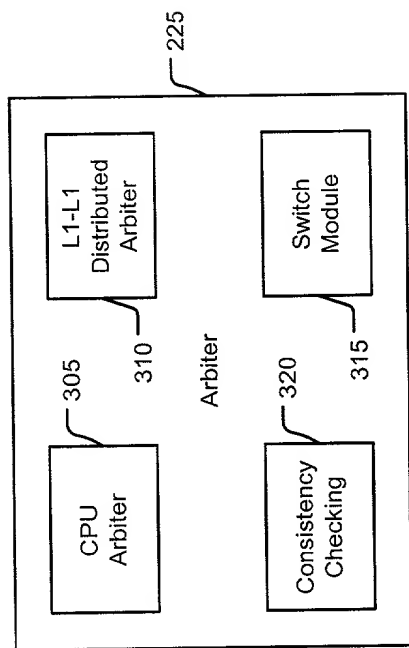
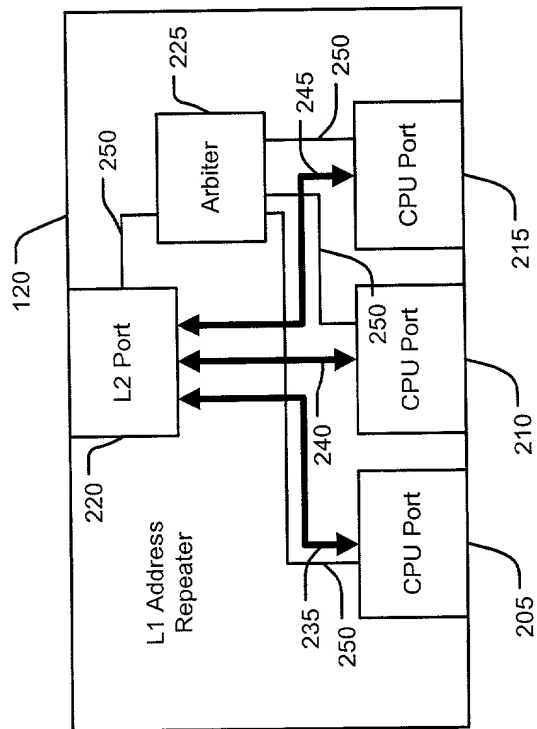


Figure 4(a) is a block diagram of a CPU port 400. The CPU port 400 is connected to a CPU-L1 bus 410. The CPU port 400 includes an incoming request queue (IRQ) 405. The CPU port 400 is connected to the CPU-L1 bus 410 via a multiplexer 410. The CPU port 400 is connected to the CPU-L1 bus 410 via a multiplexer 410. The CPU port 400 is connected to the CPU-L1 bus 410 via a multiplexer 410.

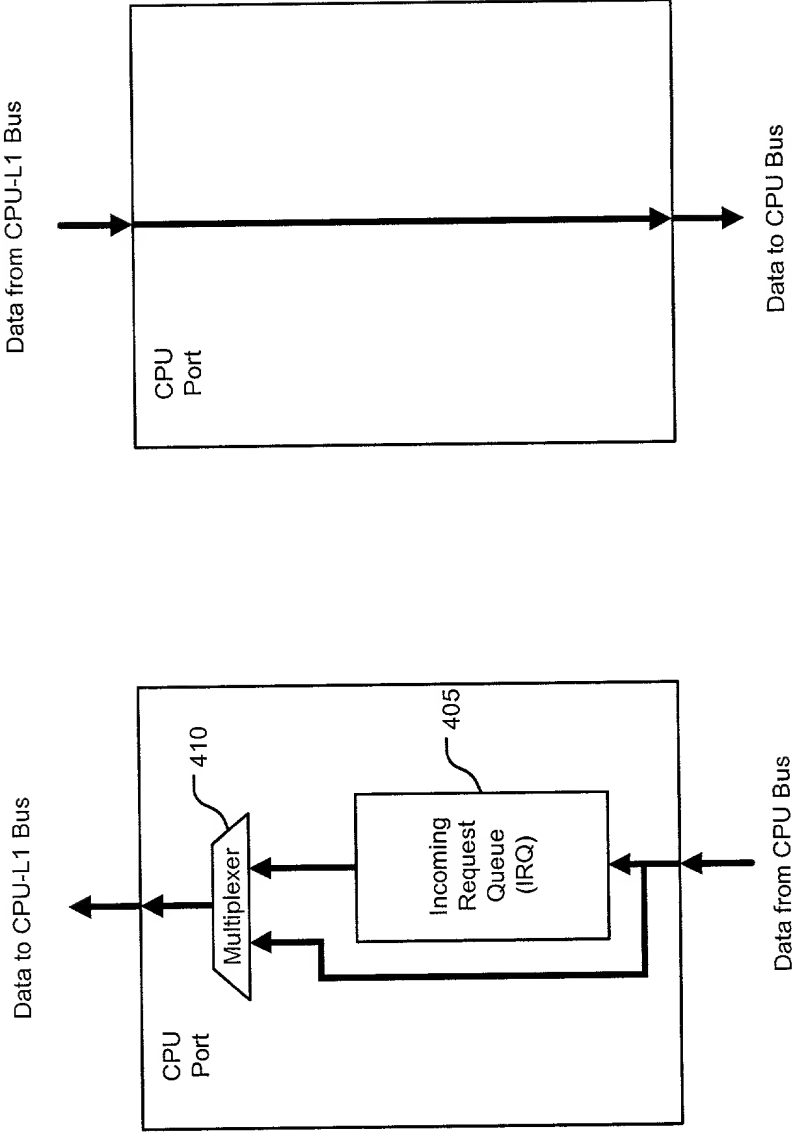


Figure 4(a)

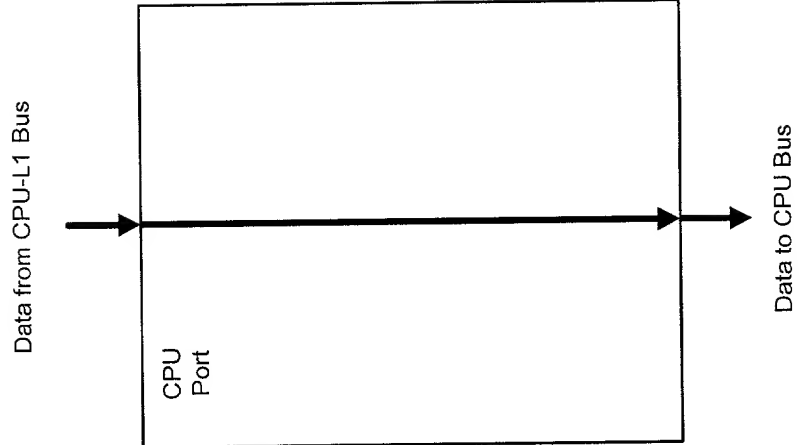


Figure 4(b)

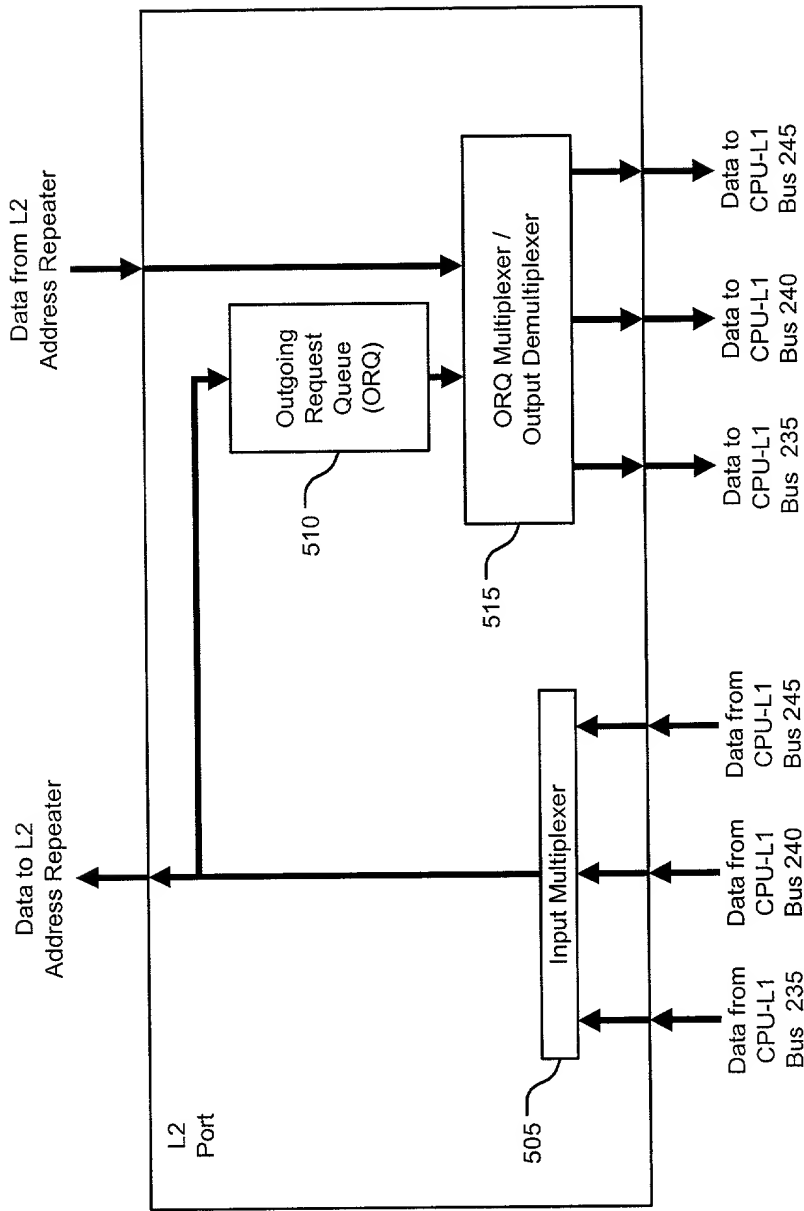
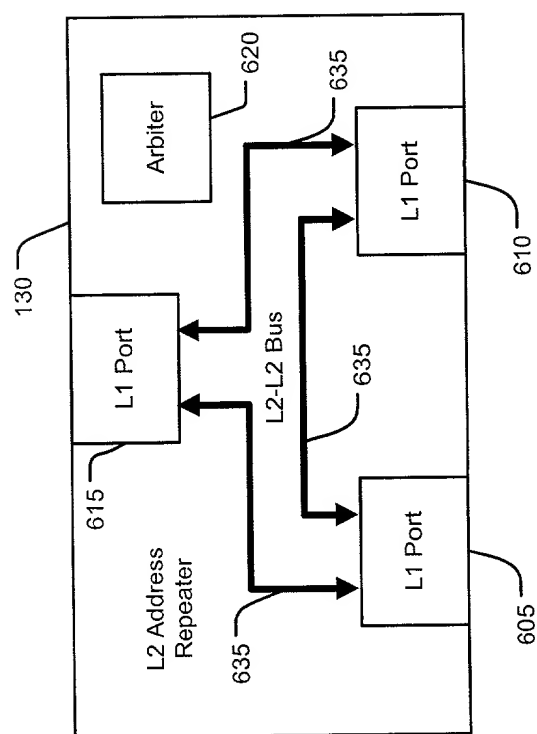


Figure 5



**Figure 6**

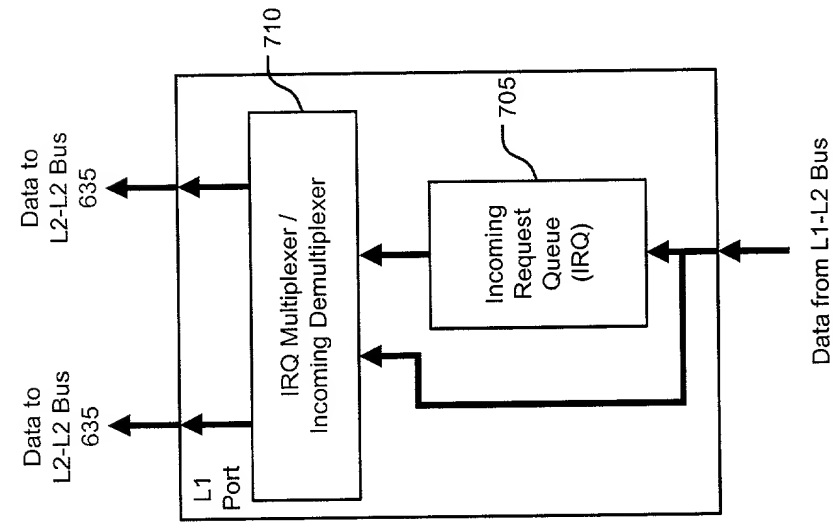


Figure 7(a)

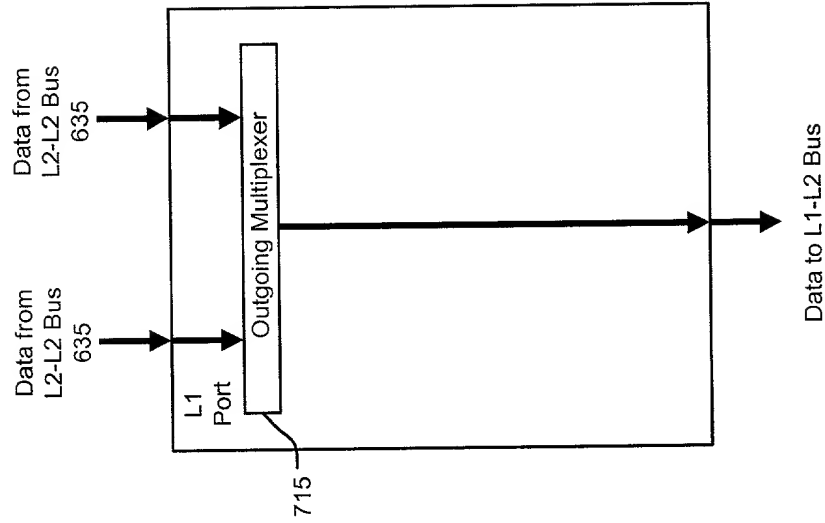


Figure 7(b)

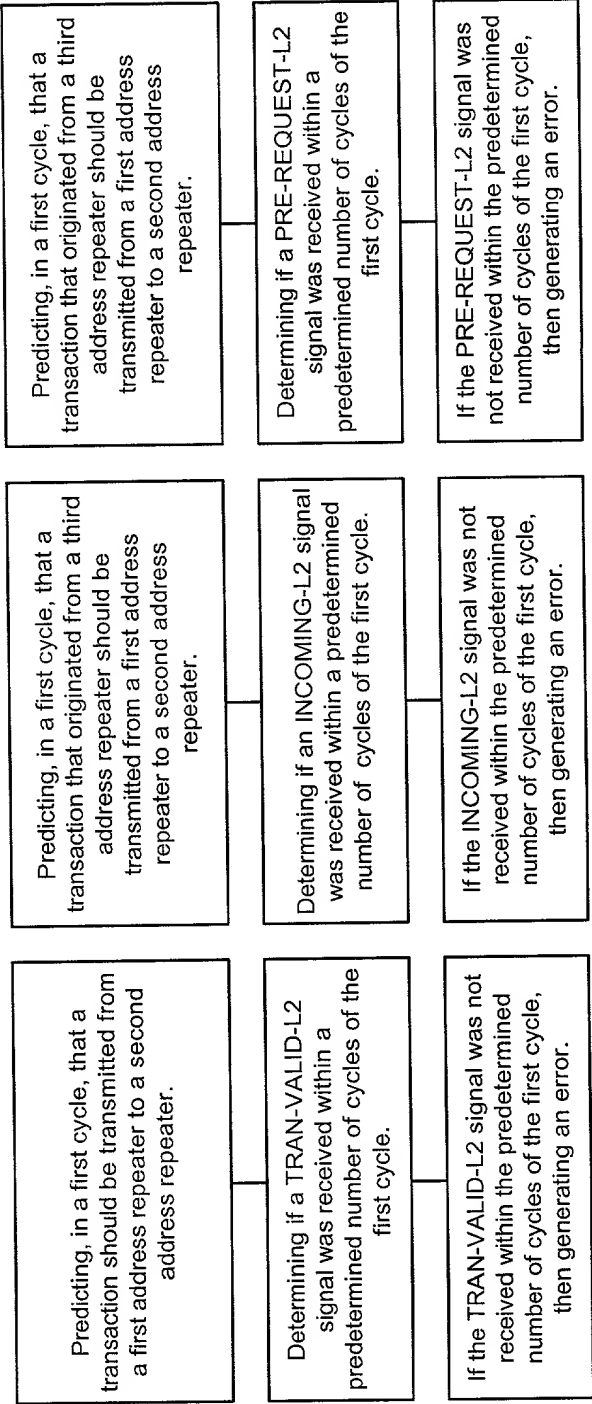


Figure 8(a)

Figure 8(b)

Figure 8(c)

